DOCKET NO: 211200US2



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

: EXAMINER: TRIMMINGS, J. P. **TOSHITADA SAITO**

SERIAL NO: 09/900,945

: GROUP ART UNIT: 2133 RCE FILED: MARCH 29, 2005

FOR: ONE-CHIP SYSTEM LARGE-SCALE INTEGRATED CIRCUIT

INCLUDING PROCESSOR CIRCUIT AND

ITS PERIPHERAL CIRCUITS

REQUEST FOR RECONSIDERATION

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated May 16, 2005, please reconsider the aboveidentified application as follows:

Remarks/Arguments begin on page 2 of this paper.